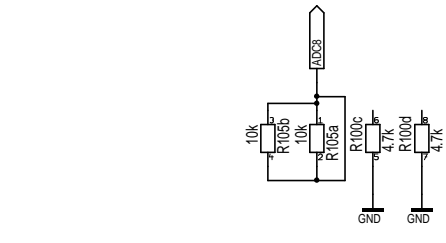
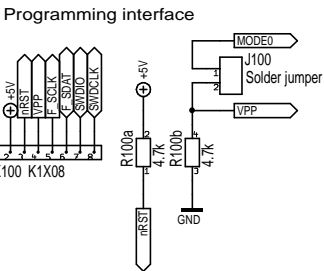
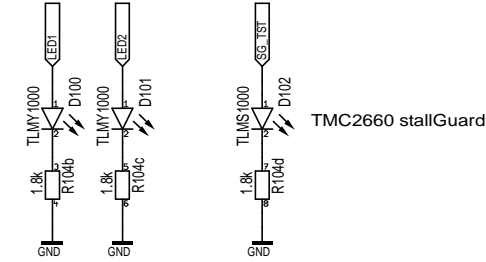
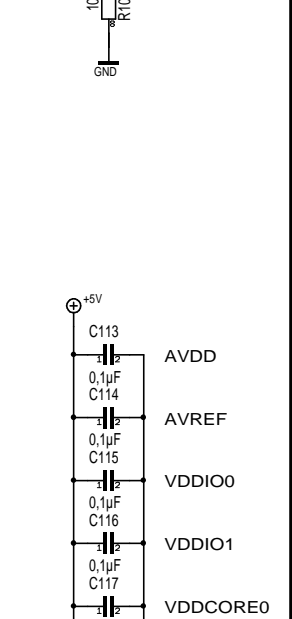
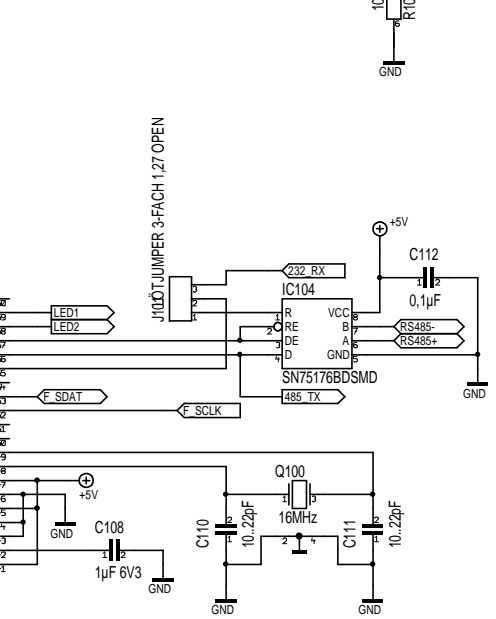
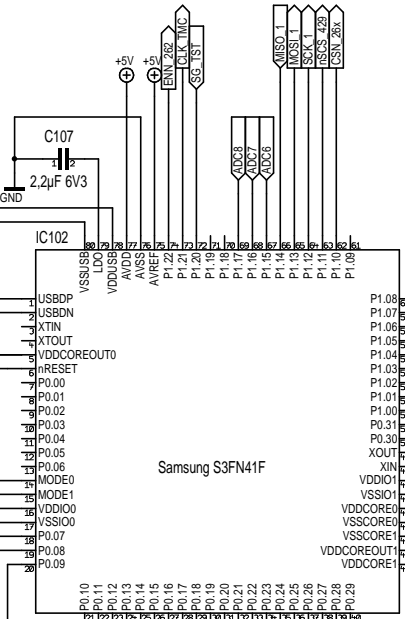
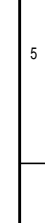
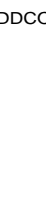
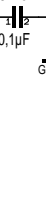
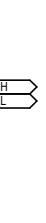
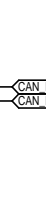
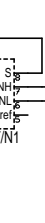
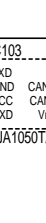
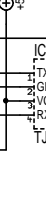
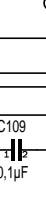
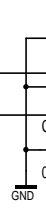
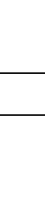
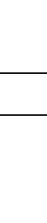
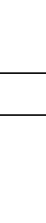
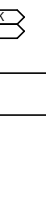
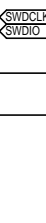
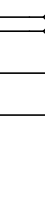
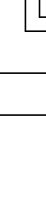
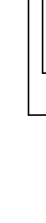
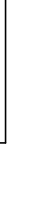
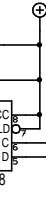
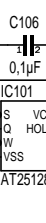
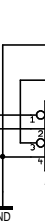
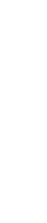
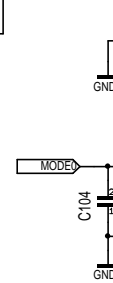

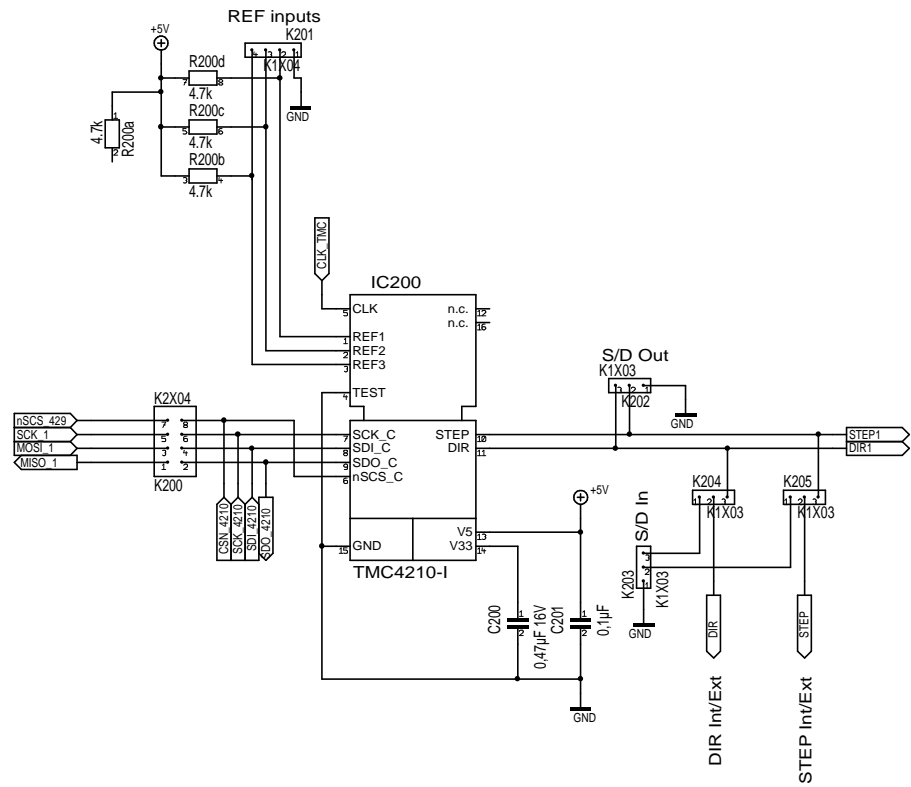



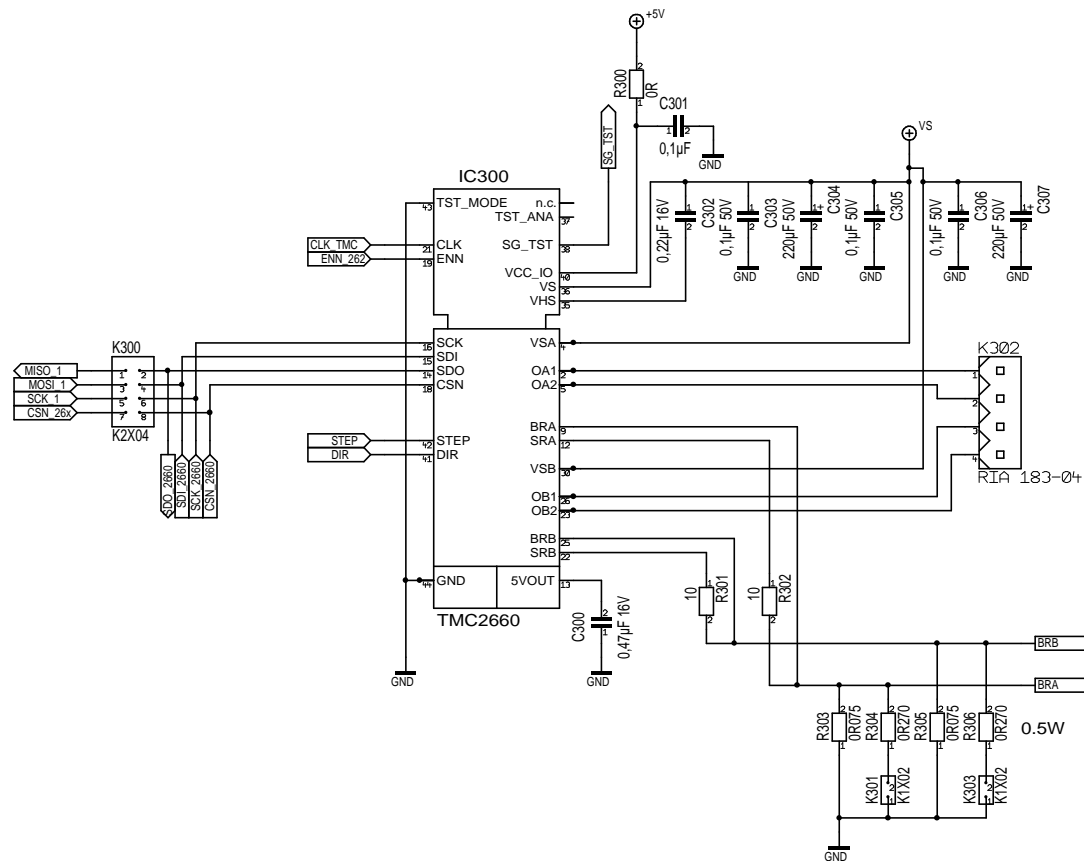
R102/103 = 0R - internal resistance in CPU
see Datasheet S3FN41F_UM_REV1.60 page 26-23




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				TMC4210+TMC2660-Eval-V11	
				CPU	
				Sheet 1 of 3	
Version	Change	Date	Name	Subject to changes in design	



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				Sheet 2 of 3	
Version	Change	Date	Name	Subject to changes in design	



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				TMC2660	
				Sheet 3 of 3	
Version	Change	Date	Name	Subject to changes in design	